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DC		Al-Assadi et al, "Pass-Transistor Logic Design", <i>Int. J. Electronics</i> , 70(4):739-749, 1991			
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DC		Ozdag et al, "High-Speed QDI Asynchronous Pipelines", <i>Proc. 8th Int. Symposium on Asynchronous Circuits and Systems</i> , April, 2002, pp. 1-10			
DC		Morgenshtein et al, "Gate-Diffusion Input (GDI) - A Novel Power Efficient Method for Digital Circuits: A Design Methodology", <i>14th Annual IEEE International ASIC/SOC Conference</i> , Sept., 2001, pp. 39-43			
DC		Morgenshtein et al, "Gate-Diffusion Input (GDI): A Power Efficient Method for Digital Combinatorial Circuits", <i>IEEE Trans. VLSD Systems</i> , 10(5):566-581, Oct., 2002			
DC		Morgenshtein et al, "Asynchronous exaggerate Gate-Diffusion Input (GDI) Circuits", <i>ISCAS 2002 IEEE International Symposium on Circuits and Systems</i> , May, 2002, pp. 1-8 pp. 1-8			
DC		David et al, "An Efficient Implementation of Boolean Functions as Self-Timed Circuits", <i>IEEE Trans. On Computers</i> , 41(1):2-11, 1992			
De		Alidina et al, "Precomputation-Based Sequential Logic Optimization for Low Power", <i>IEEE Trans. VLSD Systems</i> , 2(4):426-436, 1994			
DC		Zimmerman et al, "Low-Power Logic Styles: CMOS Versus Pass-Transistor Logic", <i>IEEE J. Solid-State Circuits</i> , 32(7):1079-1090, 1997			
DC		Yano et al, "Top-Down Pass-Transistor Logic Design", <i>IEEE J. Solid-State Circuits</i> , 31(6):792-803, 1996			
DC		Chandrakasan et al, "Low-Power CMOS Digital Design", <i>IEEE J. Solid-State Circuits</i> , 27(4):473-484, 1992			
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